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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Frank O'Mahony et al.

Title: LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC CIRCUITS

Docket No.: 884.405US1

Serial No.: 09/893,023

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Commissioner for Patents  
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

☒ A return postcard.

☒ An Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 15 cited references.

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 11 day of September, 2002.

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